FIG.1 Background Art

1										
	DMA1									
lead-in	reserved									
area	DMA2									
	reserved									
<b>†</b>	user area in group 1	spare area								
·	user area in group 2	spare area								
I	•	•								
data area	:	·								
	user area in group N-1	spare area								
	user area in group N	spare area								
1	DMA3									
lead—out	reserved									
area	DMA4									
	reserved									
		·								

FIG.2A Background Art

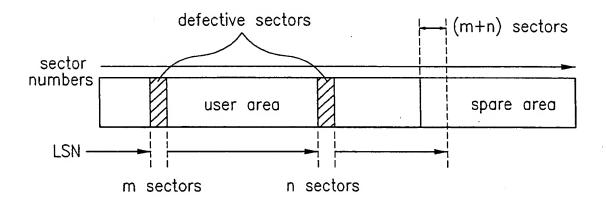


FIG.2B Background Art

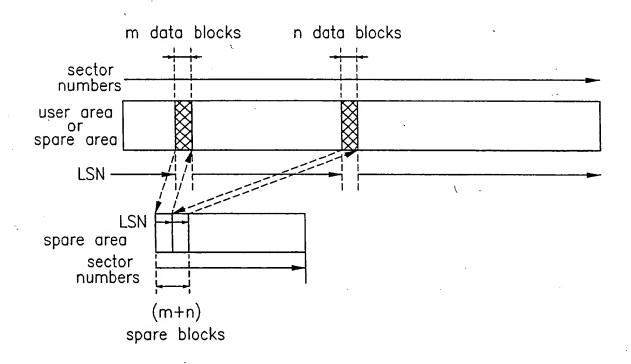


FIG.3 Background Art

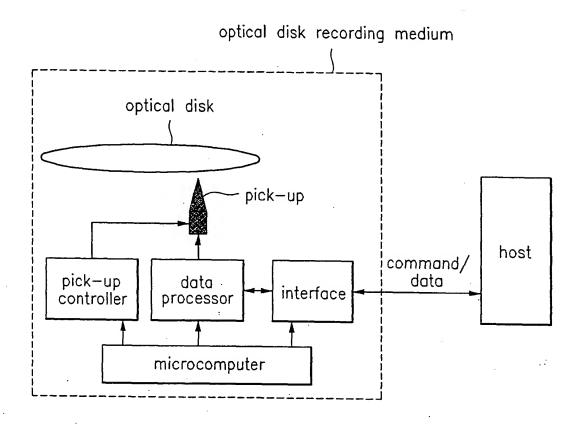


FIG.4A Background Art

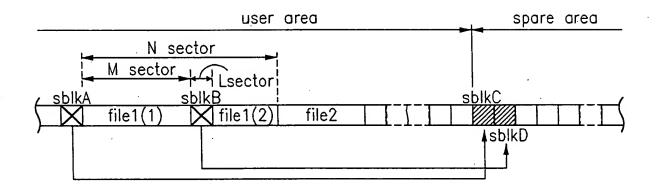


FIG.4B Background Art

	starting sector address	sector number
file1	A	N

FIG.4C Background Art

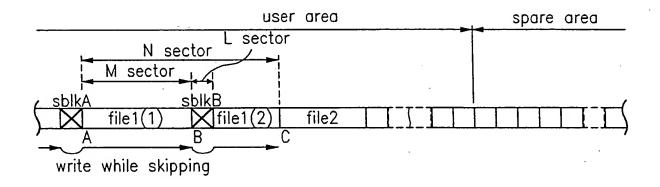


FIG.4D Background Art

	starting	sector	address	number	of	sectors
first representing method for file 1		Α			N	
second representing method for file 1		Α		N	-L	

FIG.5

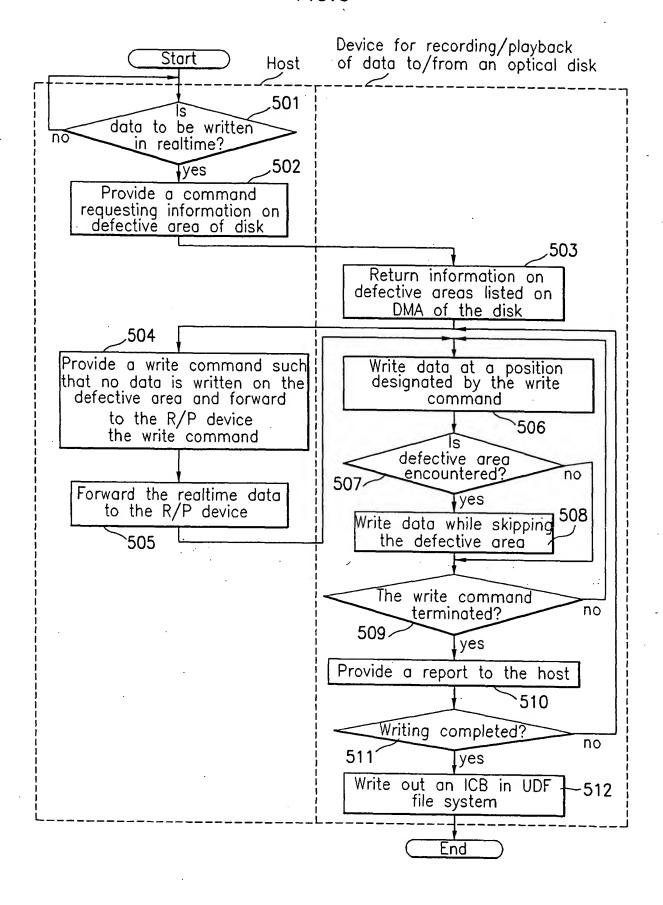
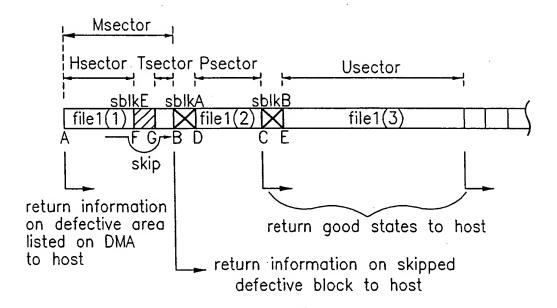


FIG.6A



: defective block not listed on SDL

 ${f igwedge}$  : defective block listed on SDL

FIG.6B

0					_																
-	i.	Sense Key	)	·										. [					٠		
2	70h)	Sens					١						alifier	ode							
3	Error Code(70h)			1:00			Additional Sense Length	, T		7	0	Additional Sense Code	Additional Sense Code Qualifier	Field Replaceable Unit Code							
4	Error	Reserved		Information			tional Ser	3	reserved	recented	2001	litional Se	al Sense	Replaceab		LSN1			LSN2		
5		ΠI				:	Addii					Adc	Addition	Field							
9																					:
7																					
Byte Bit	0 -	2	3	4	5	9	7	8	6	10	11	12	13	14	15	16	17	18	19	20	

FIG.6C

	starting sector address	number of sectors
	А	Н
file 1	G	Т
1116	D	Р
	E	. U

FIG.7

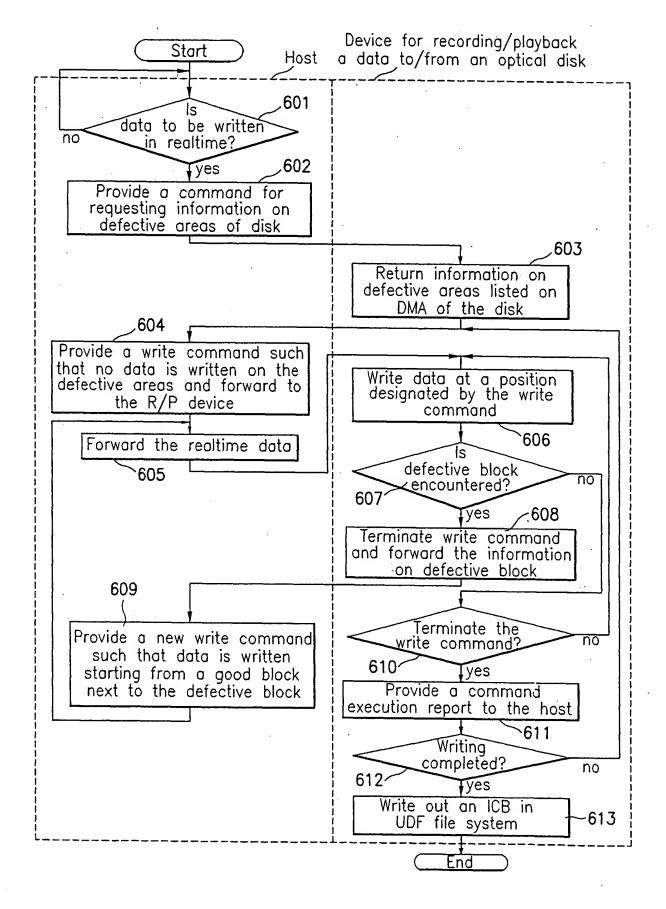
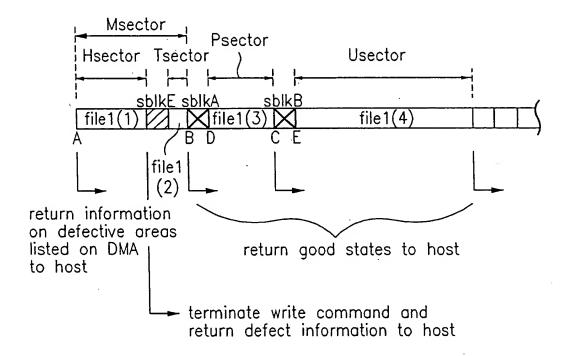


FIG.8A



: defective block not listed on SDL

: defective block listed on SDL

0															
-			Sense Key												
2	70h)		Sens							a.	mhor	i noci		alifier	ode
3	Error Code (70h)	ved				uoi		se Length	o que	nanna	104000	פכנטו וומ	nse Code	Code Quo	e Unit Co
4	Ē	Reserved	Reserved		7 1	Intormation		Additional Sense Length	70,000	iisted sector iiniiibei	of it of of	כחוואפרמוואפ מפופרוואפ אפרוסו וומווותפו	Additional Sense Code	Additional Sense Code Qualifier	Field Replaceable Unit Code
5			]]]					Addit	40:1	IISIE	7 7 17 100	a annac	Add	Addition	Field R
9			;												-
7															
Byte Bit	0		2	3	4	5	9	7	∞	თ	10	11	12	13.	14

FIG.9

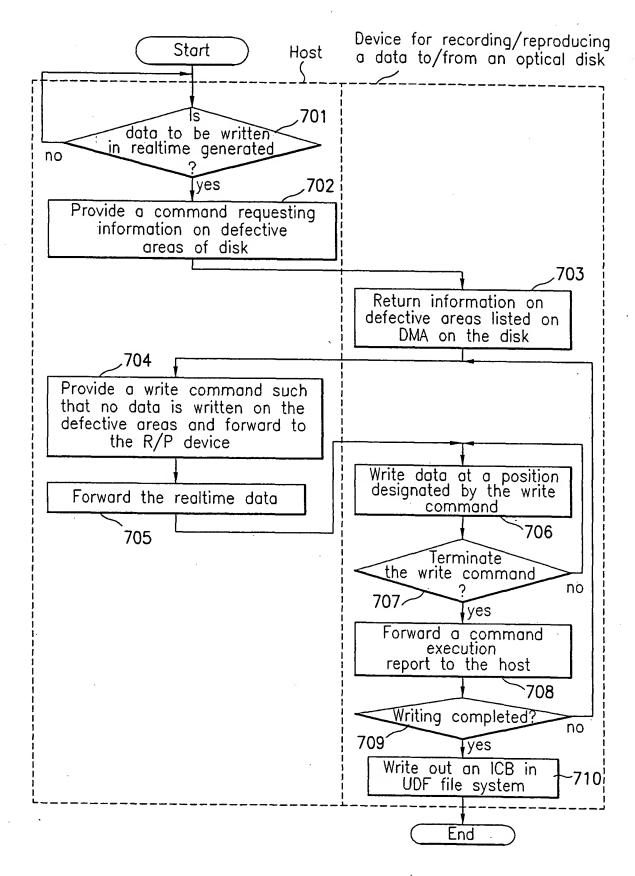
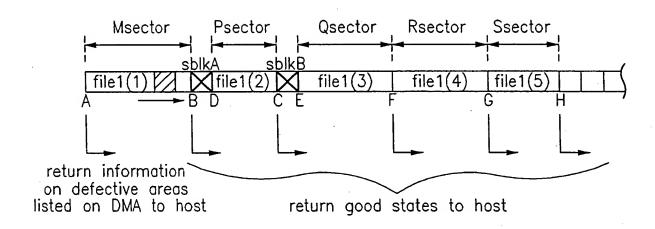


FIG.10A



: defective blocks not listed on SDL

FIG.10B

	starting sector address	sector number
	A	М
file 1	D	Р
	E	Q + R + S

FIG.11

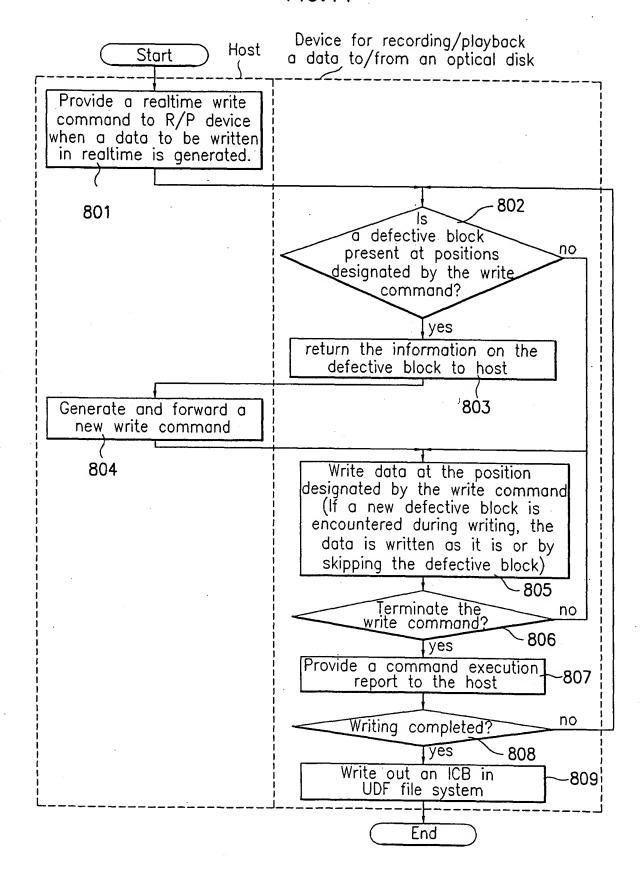
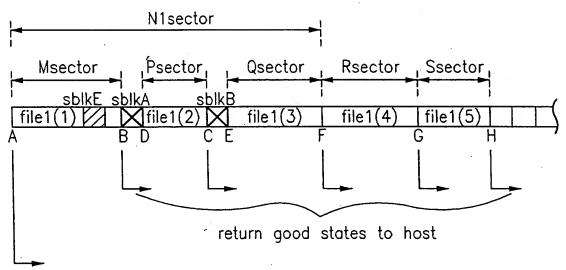


FIG.12



return defect information listed on SDL within N1 write command to host

: defective blocks not listed on SDL

 $\boxtimes$  : defective blocks listed on SDL